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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/845,469	04/30/2001	Dejan S. Milojicic	10003509-1	3049	
75	590 01/30/2004	EXAMINER			
	ACKARD COMPAN	IQBAL, N	IQBAL, NADEEM		
Intellectual Property Administration			ART UNIT	PAPER NUMBER	
P.O. Box 27240	-	ARTONIT	TATER NOMBER		
Fort Collins, CO 80527-2400			2114	3 .	
			DATE MAILED: 01/30/2004	4 4	

Please find below and/or attached an Office communication concerning this application or proceeding.

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		App	lication No.	/	Applicant(s)			
Office Action Summary		09/	845,469	ľ	MILOJICIC ET AL.			
		Exa	miner		Art Unit			
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Period fo	The MAILING DATE of this commu or Reply	inication appears	on the cover sheet	with the cor	respondence ad	Idress		
THE - External after aft	MAILING DATE OF THIS COMMUL mensions of time may be available under the provision of SIX (6) MONTHS from the mailing date of this con- period for reply specified above is less than thirty of period for reply is specified above, the maximum are to reply within the set or extended period for re- preply received by the Office later than three month ed patent term adjustment. See 37 CFR 1.704(b).	NICATION. ns of 37 CFR 1.136(a). I nmunication. (30) days, a reply within statutory period will appl oly will, by statute, cause	n no event, however, may the statutory minimum of t y and will expire SIX (6) Mi the application to become	a reply be timely hirty (30) days w ONTHS from the ABANDONED	y filed will be considered timel mailing date of this c (35 U.S.C. § 133).	ly. ommunication.		
1)⊠	Responsive to communication(s) f	iled on <u>30 April 20</u>	<u>001</u> .					
2a) <u></u> ☐	This action is FINAL .	2b)⊠ This action	n is non-final.					
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposit	ion of Claims							
5)⊠ 6)⊠ 7)⊠	 ✓ Claim(s) 1-19 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. ✓ Claim(s) 9 is/are allowed. ✓ Claim(s) 1,2,5-8,10,11,14-16 and 19 is/are rejected. ✓ Claim(s) 3,4,12,13,17 and 18 is/are objected to. ✓ Claim(s) are subject to restriction and/or election requirement. 							
Applicat	ion Papers							
10) 11)	The specification is objected to by The drawing(s) filed on is/ar Applicant may not request that any ob Replacement drawing sheet(s) including The oath or declaration is objected	e: a) accepted jection to the drawing the correction is	ng(s) be held in abey required if the drawir	rance. See 3 ng(s) is objec	37 CFR 1.85(a). cted to. See 37 C	` ,		
_	under 35 U.S.C. §§ 119 and 120	6 6) C 440/-) /	(-1) (5)			
* (13)	Acknowledgment is made of a clai All b) Some * c) None of 1. Certified copies of the priorit 2. Certified copies of the priorit 3. Copies of the certified copie application from the Internat See the attached detailed Office act Acknowledgment is made of a claim ince a specific reference was included 7 CFR 1.78. Acknowledgment is made of a claim eference was included in the first see	y documents hav y documents hav s of the priority do ional Bureau (PC ion for a list of the for domestic prio led in the first sen anguage provision for domestic prio	e been received. e been received in ocuments have bee T Rule 17.2(a)). e certified copies no rity under 35 U.S.C tence of the specif hal application has rity under 35 U.S.C	Application en received. C. § 119(e) fication or in been received. C. §§ 120 a	No in this National (to a provisional an Application ved. nd/or 121 since	I application) Data Sheet. a specific		
Attachmer	nt(s)							
2) 🔲 Notic	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review mation Disclosure Statement(s) (PTO-1449)				TO-413) Paper No(ent Application (PTC			

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DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).
- 3. Claims 1, 2, 5-8, 10, 11, 14-16, & 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Vivio et al., (U.S. Patent number 5867642) in view of Hashiguchi et al., (U.S. Patent number 5261084).
- 4. As per claim 1, Vivio et al., (Vivio) teaches (col. 2, lines 60-63) a system management controller that logs memory errors detected by an error detection and correction memory system. He thus teaches limitations pertain to identifying a predetermined instruction sequence, monitoring for memory access errors and logging errors. He does not explicitly disclose raising exceptions if the memory access error is logged. Hashiguchi et al., teaches (col. 2, lines 27-29) that when the data memory access error is detected, exceptional processing is automatically

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executed. A person of ordinary skill in the art at the time the invention was made would have been motivated to include to perform raising exceptions when memory access error is logged into the invention of Vivio from the invention of Hashiguchi since Vivio teaches to log non-fatal memory errors and clearly would have been desirable to perform exceptional processing for other types of errors, thus providing motivation for a person of ordinary skill in the art for the stated inclusion.

5. As per claim 2, Vivio teaches (col. 2, lines 52-54) a system that dynamically remaps data from a potentially faulty memory area to reserve memory area while maintaining memory coherency. He thus teaches to provide checkpointing a set of system data and recovering from the memory access error.

Allowable Subject Matter

- 6. Claim 9 is allowed.
- 7. Claims 3, 4, 12, 13, 17 & 18 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 8. As per claims 5 & 6, He also teaches (col. 2, lines 57-59) that his invention avoids errors fro devices and applications that can not tolerate long delays in accessing memory, thus providing masking a machine check abort handle.
- 9. As per claim 7, Vivio already teaches (col. 2, lines 60-63) a system management controller that logs memory errors detected by an error detection and correction memory system, He thus would also update pointers.

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- 10. As per claim 8, Vivio teaches (col. 2, lines 55-57) to perform remapping of memory without denying accesses to memory or interrupting applications for relatively long periods of time, thus would allow re-executing the memory access request.
- 11. As per claims 10 & 15, Vivio substantially teaches the claimed invention as disclosed related to claim 1 above. He also teaches a system controller that logs memory errors detected by an error detection and correction memory system. He thus teaches limitations pertain to identifying a predetermined instruction sequence, monitoring for memory access errors and logging errors. He does not explicitly disclose raising exceptions if the memory access error is logged. Hashiguchi et al., teaches (col. 2, lines 27-29) that when the data memory access error is detected, exceptional processing is automatically executed. A person of ordinary skill in the art would have been motivated to include to perform raising exceptions when memory access error is logged into the invention of Vivio from the invention of Hashiguchi since Vivio teaches to log non-fatal memory errors and clearly would have been desirable to perform exceptional processing for other types of errors, thus providing motivation for a person of ordinary skill in the art for the stated inclusion.
- 12. As per claims 11 & 16, Vivio teaches (col. 2, lines 52-54) a system that dynamically remaps data from a potentially faulty memory area to reserve memory area while maintaining memory coherency. He thus teaches to provide checkpointing a set of system data and recovering from the memory access error.
- 13. As per claim 19, He also teaches (col. 2, lines 57-59) that his invention avoids errors fro devices and applications that can not tolerate long delays in accessing memory, thus providing masking a machine check abort handle.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nadeem Iqbal whose telephone number is (703)-308-5228. The examiner can normally be reached on M-F (8:00-5:30) First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert W Beausoliel can be reached on (703)-305-9713. The fax phone number for the organization where this application or proceeding is assigned is (703)-872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (7.03) \$\frac{2}{3}05-3900\$.

Nadeen Iqbal Primary Examiner Art Unit 2114